BaseSpec: Comparative Analysis of Baseband Software and Cellular Specifications for L3 Protocols

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Processors in smartphone





Baseband can be attacked!





Cellular Protocol Stack

- Baseband handles control plane protocols
 - ~100 documents (each has hundreds of pages)
- ✤ Layer 3 (L3) includes core procedures
 - Call control (CC), Mobility management (MM), session management (SM)
- Multiple vulnerabilities have been found in Layer 3





Analyzing Baseband Security

- Challenge: Obscurity vendors do not release details of baseband
- Manual analysis
 - Baseband Attacks (Weinmann, WOOT'12)
 - Breaking Band (Golde et al., REcon'16)
 - A walk with Shannon (Cama, OPCDE'18)
 - → Limited scalability and applicability
 - Numerous functions (over 90K) for processing hundreds of messages
 - Diverse firmware versions and device models
- Dynamic analysis
 - SMS of Death (Mulliner et al., Security'11)
 - Security testing of GSM implementations (Broek et al., ESSoS'14)
 - BaseSAFE (Maier et al., WiSec'20)
 - → Hard to automate
 - Numerous non-trivial operations (e.g., mobility, session, call, ...)
 - Dynamic analysis finds only shallow bugs (e.g., crash)



Observation

- ✤ Baseband is software for network communication
 - Receive radio signals
 - **Decode** messages
 - Send responses or update states
- Decoder should implement protocol specifications (hundreds of messages)





Our Approach - BaseSpec

- ✤ Comparative analysis of baseband and specification
 - Focusing on protocol decoding logic



Message structures are embedded in a machine-friendly form

- → Comparing the structures with the documented specification can be **automated**
- ✤ Main decoding logic rarely changes
 - → Once analyzed, **applicable** to various firmware versions and device models



BaseSpec Overview



BaseSpec System



Processing Specification



➔ Ground Truth



Extracting Msg. Structures from Spec.

- 1) Download spec documents from the 3GPP (.doc) and ETSI (.pdf) websites
- 2) Convert documents to raw text
- 3) Handle inconsistencies (documents are written in a natural language)
- 4) Parse message structures





Standard L3 Messages

✤ Have a standardized form



- ✤ Message: set of Information Element (IE)
- ✤ An IE can have three elements
 - IEI: IE Identifier (T), Length (L), Value (V)
- ✤ An IE can be mandatory or optional



IEI	Information Element	Type/Reference	Presence	Format	Length
	Protocol discriminator	Protocol discriminator	М	V	1/2
	Security header type	Security header type 9.3.1	м	V	1/2
	Attach reject message identity	Message type 9.8	м	V	1
	EMM cause	EMM cause 9.9.3.9	М	V	1
78	ESM message container	ESM message container 9.9.3.15	0	TLV-E	6-n
		:			

Table 8.2.3.1: ATTACH REJECT message content



Г	IEI	Information Element	Type/Reference	Presence	Format	Length
╺		Protocol discriminator	Protocol discriminator 9.2	М	V	1/2
╺		Security header type	Security header type 9.3.1	М	V	1/2
		Attach reject message identity	Message type 9.8	М	V	1
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Table 8.2.3.1: ATTACH REJECT message content

IE: Information Element

Presence: Mandatory (M), Optional (O)

IEI: Information Element Identifier



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Table 8.2.3.1: ATTACH REJECT message content

IE: Information Element

Presence: Mandatory (M), Optional (O) IEI: Information Element Identifier Format: IEI (T), Length (L), Value (V), Extended (-E)







Processing Firmware



➔ Our Analysis Target



Firmware Analysis





Firmware Analysis

- ✤ Challenge
 - **Obscurity** vendors do not open firmware details
- Target
 - Firmware from 2 major vendors (architecture: ARM)
- Method
 - Manual analysis to uncover the firmware's obscurity
 - Extract decoder function and message structure information
- → After one-time **manual** analysis, can be **automated**



Firmware Analysis

- Preprocessing
 - Firmware extraction
 - Memory layout analysis
 - Function boundary identification
- ✤ Identify the L3 decoder
 - Utilize debug information
 - "L3", "Decode", "EMM", ...
- Analyze embedded specification
 (= embedded message structures)





Typical Firmware (Multiple Binaries) Baseband Firmware (Single Binary)

DCD	0xFECDBA98	
DCD	aWarnDecodeErro	; "Warn>Decode Error: 0x%x"
DCD	0xC28	
DCD	asc_4156E7E0	; "//CALPSS/LteL3/LteSae/
	Sample De	bug Information



Msg. Structures in Vendor₁ Firmware

✤ 4 types of linked lists



→ Contains IE information for every implemented message



Finding Syntactic Error





Syntactic Comparison

- Check whether the embedded structures are correct
 - Directly indicate developers' mistakes (e.g., mistyping length)





Syntactic Comparison

- Check existence / length
- ✤ Correct
- ✤ Invalid Mismatch
 - Incorrect length
- Missing Mismatch
 - Not in firmware
- Unknown Mismatch
 - Only in firmware

	From Spe	cificatio)n		From F	irmware
IEI	IE Name	Format	Value Length	_	IEI	Value Length
-	Protocol disc	V	1/2	_	-	-
-	Security header	V	1/2	_	_	-
-	Attach reject	V	1	_ Correct	_	-
-	EMM cause	V	1		_	1
78	ESM	TLV-E	3- n		78	<mark>0</mark> -n
5 F	T3346 value	TLV	1	Missing	-	-
				Unknown 📫	FF	1

From Specification



Finding Semantic Error





Semantic Comparison

- Check whether the decoder operates correctly
 - Can identify missing logic (e.g., length check) or exceptional cases
- ✤ Use symbolic execution to analyze the decoding logic





Semantic Analysis

- Run symbolic execution on the decoder function
 - Collect symbolic variables and constraints
 - Created when the decoder checks IE Identifiers (IEIs) or lengths
 - Identify IE Identifier (IEI) and Length Indicator (LI) and build message structures
 - Compared with specifications to find mismatches (invalid, missing, and unknown)





Implication Analysis





Implication Analysis

- Comparison reports mismatches
 - Missing: IEs not in firmware
 - Unknown: IEs only in firmware
 - Invalid: IEs with incorrect lengths



- Some mismatches may not cause errors
 - Additional check routines after decoder function
 - Optional to implement
 - → Manual analysis is required

But mismatches can pinpoint erroneous parts



Evaluation

- Implemented a prototype with IDA Pro and angr
- Target
 - 2 major vendors (ARM)
 - 18 firmware images from **Vendor**₁ (9 models × 2 versions)
 - 3 firmware images from Vendor₂ (3 models)
- Details are anonymized upon the vendor's request
 - We reported all the findings to the vendors



Evaluation Results

- Hundreds of mismatches are reported from every firmware
- Implication analysis results
 - Vendor₁
 - 5 Functional Errors
 - 4 Memory-related vulnerabilities
 - → 2 critical Remote Code Execution (RCE) vulnerabilities
 - Vendor₂
 - 1 Memory-related vulnerability



# c Model Build Date Msgs	of s IEs	Missing	Unknown	Invalid	Missing	Unknown	T11.1	10.1			+	
Model Build Date Msgs	s IEs	i-IE n-IE			0	UIKIOWI	Invalid	Missing	Unknown	Invalid	Functional	Memory-related
			i-IE n-IE	i-IE n-IE	i-IE n-IE	E1 E2 E3 E4 E5	E6 [‡] E7 E8 [‡] E9					
B Model B May/2020 268 Model C May/2020 268 Model C May/2020 268 Model D Jun/2020 268 Model E Jun/2020 268 Model F Apr/2020 268 Model G Apr/2020 268 Model H Apr/2020 268 Model I Apr/2020 268	1204 1201 1201 1200 1200 1198 1198 1096 1096	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	35 203 35 200 35 200 32 186 32 186 32 186 32 186 32 186 32 186 32 186 32 186 32 71 32 71	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

System Security Lab

- ✤ Missing imperative (≈mandatory) & Unknown IEs
 - Directly indicate functional errors

			innon witsin	atch	Synta	ctic-only Mi	smatch	Semar	ntic-only Mi	smatch	Case Stu	dy Results
	# of	Missing	Unknown	Invalid	Missing	Unknown	Invalid	Missing	Unknown	Invalid	Functional [†]	Memory-related
Model Build Date Ms	lsgs IEs	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	E1 E2 E3 E4 E	5 E6 [‡] E7 E8 [‡] E9
Model A May/2020 26 Model B May/2020 26 Model C May/2020 26 Model C Jun/2020 26 Model E Jun/2020 26 Model F Apr/2020 26 Model G Apr/2020 26 Model H Apr/2020 26 Model I Apr/2020 26	68 1204 68 1201 68 1201 68 1200 68 1200 68 1198 63 1096 63 1096	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{ccccc} 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 3 \\ 0 & 3 \\ 0 & 3 \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		

33 *IE: Information Element (= message field)

i-IE: imperative (≈mandatory) IE

n-IE: non-imperative (≈optional) IE



- Missing imperative (\approx mandatory) & Unknown IEs *
 - Directly indicate functional errors
- Invalid IEs *

- Numerous incorrect length limit / ad-hoc length checks after decoder function —
- Can lead to memory-related bugs

	In Binary	Cor	nmon Mism	atch	Syntac	ctic-only Mi	smatch	Semar	ntic-only Mi	smatch		Case Stud	y Results
	# of	Missing	Unknown	Invalid	Missing	Unknown	Invalid	Missing	Unknown	Invalid		Functional [†]	Memory-related
Model Build Date	Msgs IEs	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE		E1 E2 E3 E4 E5	E6 [‡] E7 E8 [‡] E9
Model A May/2020 Model B May/2020 Model C May/2020 Model C May/2020 Model D Jun/2020 Model E Jun/2020 Model F Apr/2020 Model F Apr/2020 Model H Apr/2020	268 1204 268 1201 268 1201 268 1200 268 1200 268 1200 268 1200 268 1200 268 1198 263 1096 263 1096	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccc} 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 3 \\ 0 & 3 \\ 0 & 3 \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$
34 *IE: Informati	ion Elemei	nt (= me	ssage fiel	d) i-	IE: imper	rative (≈r	nandator	rv) IE	n-IE: noi	n-imperat	tive	e (≈optional) II	SysSe

- Missing imperative (\approx mandatory) & Unknown IEs *
 - Directly indicate functional errors
- Invalid IFs **

- Numerous incorrect length limit / ad-hoc length checks after decoder function —
- Can lead to memory-related bugs
- ✤ Missing non-imperative (≈optional) IEs
 - May not be buggy

	In Bin	ary		Cor	nmon	Mism	atch		5	Synta	ctic-o	nly Mi	ismate	ch		Semai	ntic-on	ıly Mi	smatch				Cas	e Stu	ıdy	Resu	lts	
	# of	f	Miss	sing	Unk	nown	Inv	alid	Mis	ssing	Unk	nown	Inv	alid	Mi	ssing	Unk	nown	Invalid			Fun	ctio	nal†	N	Mem	ory-re	lated
Model Build Date	Msgs	IEs	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE n-IE		E1	E2	E3 1	E4 E5	5 E	E6‡E	7 E8 [‡]	E9
Model A May/2020 Model B May/2020 Model C May/2020 Model C May/2020 Model D Jun/2020 Model E Jun/2020 Model F Apr/2020 Model F Apr/2020 Model I Apr/2020	$\begin{array}{cccccc} 268 & 1 \\ 268 & 1 \\ 268 & 1 \\ 268 & 1 \\ 268 & 1 \\ 268 & 1 \\ 263 & 1 \\ 263 & 1 \\ 263 & 1 \end{array}$	204 201 200 200 198 198 096 096	$ \begin{array}{c} 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ 1\\ $	164 167 167 179 179 179 179 212 212	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $	36 36 36 36 36 36 36 3 3 3 3	$38 \\ 38 \\ 38 \\ 41 \\ 41 \\ 41 \\ 41 \\ 40 \\ 40 \\ 40$	109 109 109 111 111 111 111 39 39	3 3 3 3 3 3 3 4 4	19 19 18 18 18 18 18 19 19	6 6 6 6 6 6 8 8 8	13 13 13 13 13 13 13 13 34 34	21 21 21 21 21 21 21 21 21 21 21	52 52 52 52 52 52 52 52 118 118	1 1 1 1 1 1 1 1	6 6 6 6 327 327	$\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $	9 9 9 9 9 9 1 1	35 203 35 200 35 200 32 186 32 186 32 186 32 186 32 186 32 186 32 71 32 71		~~~~~~	~~~~~~~~	~~~~~~~~~				~~~~~~~~	~~~~~~~~
35 *IE: Informat	ion Ele	men	t (=	me	ssaq	e fiel	d)	i-	IE: iı	mpe	rativ	e (≈r	nano	dator	'y) [[n-lE	: no	n-impera	tive	e (≈	≈op	otio	nal)	1E		Sys	Se

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Hodel Ruild Data Maga IFa	Missing i-IE n-IE	Unknown i-IE n-IE	Invalid	Missing	Unknown	Invalid	Missing	Unknown	Laure 12 af	Ennetien 1 [†]	M 1. 1
Model Ruild Date Maga IEs	i-IE n-IE	i-IE n-IE	· IE IE				0	UIKIIOWII	Invand	Functional	Memory-related
Model Build Date Misgs IES I			1-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	i-IE n-IE	E1 E2 E3 E4 E5	E6 [‡] E7 E8 [‡] E9
Model A May/2020 268 1204 Model B May/2020 268 1201 Model C May/2020 268 1201 Model C May/2020 268 1201 Model D Jun/2020 268 1200 Model F Jun/2020 268 1200 Model F Apr/2020 268 1198 Model G Apr/2020 263 1096 Model I Apr/2020 263 1096	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccc} 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 36 \\ 0 & 3 \\ 0 & 3 \\ 0 & 3 \end{array}$	$\begin{array}{ccccc} 38 & 109 \\ 38 & 109 \\ 38 & 109 \\ 41 & 111 \\ 41 & 111 \\ 41 & 111 \\ 41 & 111 \\ 40 & 39 \\ 40 & 39 \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccc} 0 & 9 \\ 0 & 9 \\ 0 & 9 \\ 0 & 9 \\ 0 & 9 \\ 0 & 9 \\ 0 & 9 \\ 0 & 1 \\ 0 & 1 \end{array}$	35 203 35 200 35 200 32 186 32 186 32 186 32 186 32 186 32 71 32 71	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$

36 *IE: Information Element (= message field)

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- ✤ Let's see E1 (functional) and E6 (memory-related)
 - Appear in new models (Model A to G)

		In Bi	nary		Con	nmon Mismatch					Syntac	tic-or	nly Mi	smate	ch	5	Seman	tic-on	ly Mi	smatc	h		(Case	Stud	y Re	sults	
		#	of	Mis	ssing	Unk	nown	Inv	alid	Mis	sing	Unk	nown	Inv	alid	Mis	ssing	Unk	nown	Inv	alid		Func	tiona	1†	Mer	nory-1	related
Model	Build Date	Msgs	IEs	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	IE i-IE n		i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	i-IE	n-IE	E1	E2 E	3 E4	- E5	E6 [‡]	E7 E8	‡ E9
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Case Study - E1

- Problem
 - Developers embedded IEs in an incorrect order
 - Msg 1

Msg 2

:

Buggy IEs (six IEs)

Header compression configuration Control plane only indication User data container Release assistance indication Extended protocol configuration options Serving PLMN rate control

Result



- 22 mismatches



Msg IE List

Global IE List



Case Study - E6

- ✤ Vulnerable message & IE
 - P-TMSI REALLOCATION COMMAND void handle_
 - Allocated P-TMSI IE
- Reported by invalid mismatch
 - Spec: 5 bytes
 - Firmware: takes upto 255 bytes
 - Not all IEs are checked properly
- Result
 - Stack-based buffer overflow
 - No protection (exploitable)

```
void handle_ptmsi_rellocation()
   get_ie_bytes(allocated_ptmsi,
                ALLOCATED PTMSI IDX):
    . . .
void get_ie_bytes(char *buf, enum IE_IDX idx)
   int length:
   char *value:
   // Get a length of the IE in the message (Controllable)
   length = get_ie_length(idx);
    // Check lengths for certain IEs
   if(idx == PLMN_LIST_IDX && length > 45)
       lenath = 45:
   if(idx = LSA_ID_IDX \&\& length > 3) \leftarrow No length check for
       length = 3;
                                       Allocated P-TMSI
   // Get a value of the IE (Controllable)
   value = get_ie_value(idx);
   memcpy(buf, value, length); Copy to buffer
```



Discussion & Limitations

- Fully automating bug discovery
 - Requires additional efforts for implication analysis
 - Other techniques (e.g., fuzzing, symbolic analysis) can be combined
- ✤ Applicability of BaseSpec
 - Only standard L3 messages are supported currently
 - Similar approach is applicable to other cellular protocols (e.g., ASN.1)
- ✤ Other types of bugs
 - Only covers bugs in the decoding logic
 - Cannot cover state-related bugs



Conclusion

- Systematically compared cellular baseband firmware with the specification for standard L3 messages
 - Found 10 error cases including 2 critical RCE vulnerabilities
- Lessons learned
 - Many errors occur in the development process from specifications
 - Comparative analysis can find such errors
 - Various firmware versions and device models can be analyzed (w/o real device)



Questions (1/3)

- Yeongbin Hwang
 - In the case of an encrypted message, some fields are added to the structure of the existing message. So I think the function to decode this is also a little different, can you find this case properly using a BASESPEC?

➔ Answers

- Actually, we did not have to consider encrypted messages because the decryption process is completely separated from the decoder function.
- One main key assumption of BaseSpec is that developers may follow good programming practices
 - Machine-friendly embedding of message structures rather than hard-coding everything with many if-else clauses
 - Clear separation of different tasks (decryption / parsing)



Questions (2/3)

- ✤ Tuan Hoang Dinh
 - Is this method applicable for other wireless protocols and chipsets, for example, GPS, Wifi.
- ✤ Wooyoung Go
 - If I apply this method to other cellular protocol, what should I do??
- ➔ Answers
 - The protocol should be extractable from both specification and firmware in a comparable format
 - Apply other techniques (fuzzing / emulation / source-code analysis) first if possible



Questions (3/3)

- ✤ Taehwa Lee
 - Have the vendors taken action on the vulnerabilities found?
- ✤ Youngjin Jin
 - This paper does not explicitly state any defense measures or countermeasures.
- ➔ Answers
 - The vendor₁ fixed all the bugs, but vendor 2 did not respond
 - The bugs are traditional memory corruptions (BOF)
 - Vendor₁ recently adopted stack-protector (canary)
 - However, vendor₁ wanted to anonymize the details



Thank You!

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